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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/530,375	04/06/2005	Carlos Antonio Alba Pinto	NL 020979	4877
24737	7590	04/22/2009	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			OTTO, ALAN	
P.O. BOX 3001			ART UNIT	PAPER NUMBER
BRIARCLIFF MANOR, NY 10510			2187	
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04/22/2009		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Advisory Action

Response to Amendment

1. Examiner acknowledges receipt of the amendment dated 4/9/2009. Claims 1-14 and 17-19 are pending in the application.

Response to Arguments

2. Applicant's arguments filed 4/9/2009 have been fully considered but they are not persuasive.

3. Applicant argues that Schlansker and Moroney do not teach "wherein the instruction address modification circuit includes an offset register connected to an output of a functional unit of the plurality of functional units, the functional unit updating an offset value in the offset register during the execution of the program." However, although fig. 8 of Moroney shows the translate block leading as an input to the controller, the translator 74 also accepts input from the instruction register, which is an output of the controller. As the register simply holds data, the functional unit would therefore also output to the translator.

4. Applicant argues that Schlansker and Moroney do not disclose where "the instruction address modification circuit is operationally coupled to a controller that provides the instruction address and to one of the plurality of the functional units..." Applicant argues that the controller must be distinct from the functional unit. However, that is not directly specified in the claims. Instead the wording "operationally coupled" is

used. In addition, Schlansker shows both a functional unit and a controller. The combination of Schlansker and Moroney would allow for an instruction address modification circuit that is operationally coupled to both a controller and a functional unit.

5. Applicant also argues that the official notice from the rejection of claim 6 should be described in documents. However, Tremblay (U.S. Patent No. 5,875,483) was cited in the action of 12/19/2007 and teaches that multiple virtual addresses can point to the same physical address.

CLOSING COMMENTS

Conclusion

a. STATUS OF CLAIMS IN THE APPLICATION

6. The following is a summary of the treatment and status of all claims in the application as recommended by **M.P.E.P. 707.07(i)**:

a(1) CLAIMS NO LONGER IN THE APPLICATION

7. Claims 15-16 and 20 were cancelled by the amendment dated 4/9/2009.

a(2) CLAIMS REJECTED IN THE APPLICATION

8. Per the instant office action, claims 1-14 and 17-19 stand rejected.

b. DIRECTION OF FUTURE CORRESPONDENCES

9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to ALAN M. OTTO whose telephone number is 571-270-1626. The examiner can normally be reached on 8:00-5:30 M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kevin Ellis can be reached on 571-272-4205. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Alan M Otto/
Examiner, Art Unit 2187

/Kevin L Ellis/
Acting SPE of Art Unit 2187